

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-20 (cancel)

Claim 21 (new): A method comprising:

wirelessly receiving a single test command on a plurality of wireless devices formed on a wafer; and

simultaneously testing the plurality of wireless devices in response to receiving the single test command.

Claim 22 (new): The method of claim 21, further comprising switching all of the plurality of wireless devices on to wirelessly receive the single test command.

Claim 23 (new): The method of claim 22, further comprising switching on a transistor coupled to each of the plurality of wireless devices to switch all of the plurality of wireless devices on.

Claim 24 (new): The method of claim 21, further comprising wirelessly receiving the single test command without an antenna coupled to the plurality of wireless devices.

Claim 25 (new): The method of claim 24, further comprising wirelessly receiving the single test command via one or more pads formed on each of the plurality of wireless devices.

Claim 26 (new): The method of claim 24, further comprising wirelessly receiving the single test command via a power line formed on each of the plurality of wireless devices.

Claim 27 (new): The method of claim 21, further comprising communicating with individual ones of the plurality of wireless devices to receive test results.

Claim 28 (new): The method of claim 27, further comprising using a unique identifier to communicate with the individual ones of the plurality of wireless devices.

Claim 29 (new): The method of claim 21, further comprising eliminating any of the plurality of the wireless devices that does not identify itself in response to the single test command.

Claim 30 (new): The method of claim 29, further comprising next eliminating any of the plurality of wireless devices that fail a volatile memory test.

Claim 31 (new): The method of claim 30, further comprising next eliminating any of the plurality of wireless devices that fail a digital circuitry test.

Claim 32 (new): The method of claim 31, further comprising next eliminating any of the plurality of wireless devices that fail a mixed signal interface test.

Claim 33 (new): A system comprising:
a test unit to issue a single test command for transmission to a plurality of wireless devices formed on a wafer, the single test command to cause testing of the plurality of wireless devices in parallel; and

an antenna coupled to the test unit to wirelessly transmit the single test command for receipt by the plurality of wireless devices.

Claim 34 (new): The system of claim 33, further comprising a wafer sorter coupled to the test unit.

Claim 35 (new): The system of claim 33, wherein the test unit to selectively address selected ones of the plurality of wireless devices to receive test results therefrom.

Claim 36 (new): The system of claim 35, wherein the system to provide power in parallel to the plurality of wireless devices via a single power pad on the wafer.

Claim 37 (new): An apparatus comprising:
a wafer having a plurality of wireless devices formed thereon, the wafer comprising:

a power pad coupled to each of the plurality of wireless devices to provide a power supply voltage thereto during a test operation; and

a ground pad coupled to each of the plurality of wireless devices to provide a reference voltage thereto during the test operation.

Claim 38 (new): The apparatus of claim 37, further comprising a plurality of transistors, each coupled to provide the power supply voltage to a corresponding one of the plurality of wireless devices under control of a voltage on a bit pad.

Claim 39 (new): The apparatus of claim 37, wherein each of the wireless devices to directly receive a test command from a tester wirelessly.

Claim 40 (new): The apparatus of claim 37, wherein each of the plurality of wireless devices comprises a processor and a memory coupled to the processor.

Claim 41 (new): The apparatus of claim 37, wherein each of the plurality of wireless devices comprises:

an analog portion including:

a cellular radio core; and

a short-range wireless transceiver core coupled to the cellular core; and

a digital portion including:

a processor coupled to the cellular radio core and the short range wireless transceiver core to handle a plurality of wireless communication protocols; and

a memory coupled to the processor.

Claim 42 (new): The apparatus of claim 41, wherein the short-range wireless transceiver core to wirelessly receive signals from a tester without an external antenna.